

FDPC5018SG

30V Asymmetric Dual N-Channel PowerTrench® Power Clip MOSFET

Product Overview

For complete documentation, see the data sheet.

This device includes two specialized N-Channel MOSFETs in a dual package. The switch node has been internally connected to enable easy placement and routing of synchronous buck converters. The control MOSFET (Q1) and synchronous SyncFET™ (Q2) have been designed to provide optimal power efficiency.

Features

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Applications

- Computing
- Communications
- General Purpose Point of Load

Part Electrical Specifications

Product	Status	Compliance	Channel Polarity	Silicon Family	Package Name	Gate Level	Wide SOA Mosfets	Configuration	OPN Inverter Technology	$V_{(RDS(on))}Max@V_{GS}=10V(m\Omega)$	$V_{(RDS(on))}Max(V)$	$V_{(RDS(on))}Max(V)$	$I_{PM}Max(A)$	$P_{PM}Max(W)$	$R_{DS(on)}Max@V_{GS}=2.5V(m\Omega)$	$R_{DS(on)}Max@V_{GS}=4.5V(m\Omega)$	$Q_{typ}@V_{GS}=4.5V(nC)$	$Q_{typ}@V_{GS}=10V(nC)$	$Q_{typ}@V_{GS}=4.5V(nC)$	$Q_{typ}@V_{GS}=10V(nC)$	$C_{iss}Typ(pF)$	$Q_{rr}Typ(nC)$	$C_{oss}Typ(pF)$	$C_{rsw}Typ(pF)$	Package Type	Case Outline	MSL Temp(°C)	MSL Type	Container Type	Container Qty.
FDPC5018SG	Active	H Pb	N-Channel	PowerTrench®T1	Power56(SO-8FL)	Low-Medium Voltage	Logic	Dual	0	$Q_1:50Q_2:16$	12	3	$Q_1=56Q_2:109.0$	$Q_1:23Q_2:29$	-	$Q_1:65Q_2:20$	30	28	4593	-	-	-	-	PQFN-8	483ARR-PDF	1	260	REEL	3000	