

FDPC5030SG

30V Asymmetric Dual N-Channel POWERTRENCH® Power Clip MOSFET

Product Overview

For complete documentation, see the data sheet.

This device includes two specialized N-Channel MOSFETs in a dual package. The switch node has been internally connected to enable easy placement and routing of synchronous buck converters. The control MOSFET (Q1) and synchronous SyncFET™ (Q2) have been designed to provide optimal power efficiency.

Features

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Applications

- Computing
- Communications
- General Purpose Point of Load

Part Electrical Specifications

Product	Status	Compliance	Channel Polarity	Silicon Family	Package Name	Gate Type	Wide SOA MOSFETs	Configuration	OPN Inverter Technology	$V_{(RDS(on))}Max @ V_{GS} = 10V (m\Omega)$	$V_{(RDS(on))}Min (V)$	$R_{DS(on)}Max @ V_{GS} = 10V (m\Omega)$	$V_{(RDS(on))}Max (V)$	$I_{PM}Max (A)$	$P_{PM}Max (W)$	$R_{DS(on)}Max @ V_{GS} = 2.5V (m\Omega)$	$R_{DS(on)}Max @ V_{GS} = 4.5V (m\Omega)$	$Q_{typ} @ V_{GS} = 4.5V (nC)$	$Q_{typ} @ V_{GS} = 10V (nC)$	$Q_{typ} @ V_{GS} = 4.5V (nC)$	$Q_{typ} @ V_{GS} = 10V (nC)$	$C_{iss}Typ (pF)$	$Q_{rr}Typ (nC)$	$C_{oss}Typ (pF)$	$C_{rsw}Typ (pF)$	Package Type	Case Outline	MSL Temp (°C)	MSL Type	Container Type	Container Qty.
FDPC5030SG	Active	H Pb	N-Channel	PowerTrench® TT1	Power56 (SO-8 FL)	Low-Medium Voltage	Logic	0	Dual	30	12	Q1: 5.0 Q2: 2.4	3	Q1: 56.0 Q2: 84.0	Q1: 23 Q2: 29	-	Q1: 6.5 Q2: 3.0	25	18	2730	-	-	-	-	PDFN-8	483 AR-PDF	1	260	REEL	3000	